

Technical Specification RQ90138HEx22

Rail Grade DC-DC Power Supply

40-160V 200V 13.8V 300W 2000Vrms Half-brick
Continuous Input Transient Input Output Max Power Insulation DC-DC Converter





- ► Input under-voltage lockout
- Output current limit and short circuit protection
- ► Active back bias limit
- ► Output over-voltage protection
- ► Thermal shutdown

Control Features

- ► On/Off control referenced to input side
- ► Remote sense for the output voltage
- ➤ Output voltage trim range of -20%, +10%

Operational Features

- ► High efficiency, 90% at full rated load current
- ▶ Delivers full power with minimal derating no heatsink required
- ▶ Operating input voltage range: 40-160V
- ► Fixed frequency switching provides predictable EMI
- ► No minimum load requirement
- ► Meets requirements of standard EN 50155

Mechanical Features

- ► Industry standard Half-brick pin-out configuration
- ► Size: 2.386" x 2.486" x 0.495" (60.60 x 63.14 x 12.60 mm)
- ➤ Weight: 4.7 oz (133 g)
- ► Flanged baseplate version available



The RailQor® half-brick converter series is composed of next-generation, board-mountable, isolated, fixed switching frequency DC-DC converters that use synchronous rectification to achieve extremely high power conversion efficiency. Each module is supplied completely encased to provide protection from the harsh environments seen in many industrial and transportation applications.

Safety Features

PENDING

- ► Input-to-output isolation 2000Vrms
- ► UL 60950-1
- ► CAN/CSA C22.2 No. 60950-1
- ► EN 60950-1
- ► EN45545-2 R24/R25 Compliant
- ► CE Marked
- ► RoHS compliant (see last page)

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RQ90138HEx22 Electrical Characteristics(13.8 Vout)

Ta = 25 °C, airflow rate = 300 LFM, Vin = 110V dc unless otherwise noted; full operating temperature range is -40 °C to +100 °C baseplate temperature with appropriate power derating. Specifications subject to change without notice.

Parameter	Min.	Тур.	Max.	Units	Notes & Conditions
ABSOLUTE MAXIMUM RATINGS					
Input Voltage					
Non-Operating	-1		200	V	Continuous
Operating			160	V	Continuous
Operating Transient Protection			200	V	1s
Isolation Voltage					
Input to Output			2000	Vrms	60 s per EN 50155
Input to Baseplate			2000	Vrms	60 s per EN 50155
Output to Baseplate			500	Vrms	60 s per EN 50155
Operating Temperature	-40		100	°C	Baseplate temperature
Storage Temperature	-45		125	°C	
Voltage at ON/OFF input pin	-2		18	V	
INPUT CHARACTERISTICS					XX XX XX
Operating Input Voltage Range	40	110	160	V	
Input Under-Voltage Lockout					
Turn-On Voltage Threshold	34.8	37.0	39.2	V	
Turn-Off Voltage Threshold	31.9	34.1	36.3	V	
Lockout Voltage Hysteresis		2.9		V	
Input Over-Voltage Shutdown		-		V	Not Available
Recommended External Input Capacitance		100		μF	Typical ESR 0.1-0.2 Ω; See Note 2
Input Filter Component Values (L\C)		3.3\3.3		μH\μF	Internal values; see Figure D
DYNAMIC CHARACTERISTICS					
Turn-On Transient					
Turn-On Time		15		ms	Full load, Vout=90% nom.
Start-Up Inhibit Time	180	200	220	ms	Figure E
Output Voltage Overshoot			20000	uF	Maximum Output Capacitance
ISOLATION CHARACTERISTICS		$A \setminus A$			
Isolation Voltage (dielectric strength)					See Absolute Maximum Ratings
Insulation Resistance	100			MΩ	Tested at 500 Vdc per EN 50155
Isolation Capacitance (input to output)		1000		pF	See Note 1
TEMPERATURE LIMITS FOR POWER DERATIN	IG CURVES				
Semiconductor Junction Temperature			125	°C	Package rated to 150 °C
Board Temperature			125	°C	UL rated max operating temp 130 °C
Transformer Temperature			125	°C	
Maximum Baseplate Temperature, Tb			100	°C	
FEATURE CHARACTERISTICS					
Switching Frequency	255	275	295	kHz	Isolation stage switching freq. is half this
ON/OFF Control					
Off-State Voltage	2.4		18	V	
On-State Voltage	-2		0.8	V	
ON/OFF Control					Application notes Figures A & B
Pull- <mark>Up Voltage</mark>		5		V	
Pull-Up Resistance		50		kΩ	
Over-Temperature Shutdown OTP Trip Point		125		°C	Average PCB Temperature
Over-Temperature Shutdown Restart Hysteresis		10		°C	
RELIABILITY CHARACTERISTICS					
Calculated MTBF (MIL-217) MIL-HDBK-217F		TBD		10 ⁶ Hrs.	Tb = 70°C
Calculated MTBF (Telcordia) TR-NWT-000332		TBD		10 ⁶ Hrs.	Tb = 70°C
For Field Demonstrated MTBF see our website					

Note 1: Higher values of isolation capacitance can be added externally to the module.

Note 2: See "Input System Instability" in the Application Considerations section.

RQ90138HEx22 Electrical Characteristics(13.8 Vout) continued

Ta = 25 °C, airflow rate = 300 LFM, Vin = 110V dc unless otherwise noted; full operating temperature range is -40 °C to +100 °C baseplate temperature with appropriate power derating. Specifications subject to change without notice.

Parameter	Min.	Тур.	Max.	Units	Notes & Conditions
INPUT CHARACTERISTICS					
Maximum Input Current			11.1	Α	Vin min; Vout nom; in current limit
No-Load Input Current		75	110	mA	
Disabled Input Current		4	6	mA	
Response to Input Transient		0.5		V	See Figure 6
Input Terminal Ripple Current		320		mA	RMS
Recommended Input Fuse			15	Α	Fast acting external fuse recommended
OUTPUT CHARACTERISTICS					
Output Voltage Set Point	13.63	13.80	13.97	V	
Output Voltage Regulation					
Over Line		±0.1	±0.3	%	
Over Load		±0.1	±0.3	%	
Over Temperature	-207		207	mV	
Total Output Voltage Range	13.45		14.15	V	Over sample, line, load, temperature & life
Output Voltage Ripple and Noise					20 MHz bandwidth; see Note 1
Peak-to-Peak		125	250	mV	Full load
RMS		30	60	mV	Full load
Operating Output Current Range	0		21.7	Α	Subject to thermal derating
Output DC Current-Limit Inception	24.3	26.7	29.1	Α	Output voltage 10% Low
Output DC Current-Limit Shutdown Voltage		7.4		V	
Back-Drive Current Limit while Enabled		0.6		Α	Negative current drawn from output
Back-Drive Current Limit while Disabled	0	3	10	mA	Negative current drawn from output
Maximum Output Capacitance			20,000	μF	Vout nominal at full load (resistive load)
Output Voltage during Load Current Transient					
Step Change in Output Current (0.1 A/µs)		500		mV	50% to 75% to 50% Iout max
Settling Time		100		μs	To within 1% Vout nom
Output Voltage Trim Range	-20		10	%	Across Pins 8&4; Common Figures 3-5;see Note 2
Output Voltage Remote Sense Range			10	%	Across Pins 8&4
Output Over-Voltage Protection	15.8	17.0	18.2	V	Over full temp range
EFFICIENCY					
100% Load		91		%	See Figure 1 for efficiency curve
50% Load		91		%	See Figure 1 for efficiency curve

Note 1: Output is terminated with 1 µF ceramic and 15 µF low-ESR tantalum capacitors. For applications requiring reduced output voltage ripple and noise, consult SynQor applications support (e-mail: support@synqor.com)

Note 2: Trim-up range is limited below 10% at low line and full load. Contact SynQor applications support for more detail.



Input:40-160V Output:13.8V Current:21.7A Part No.:RQ90138HEx22

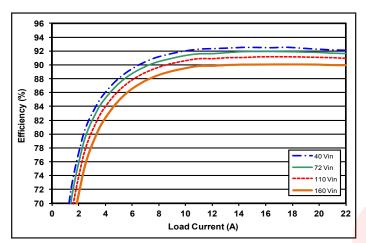


Figure 1: Efficiency at nominal output voltage vs. load current for minimum, nominal, and maximum input voltages at 25°C.

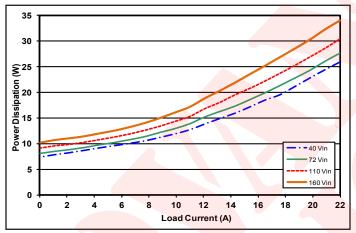


Figure 3: Power dissipation at nominal output voltage vs. load current for minimum, nominal, and maximum input voltage at Tcase=25°C.

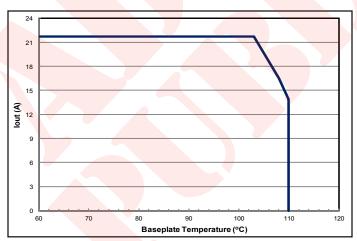


Figure 5: Maximum output current vs. base plate temperature (nominal input voltage).

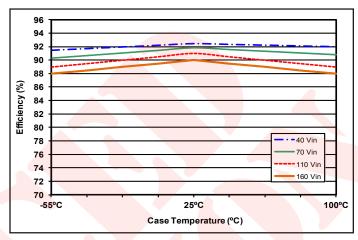


Figure 2: Efficiency at nominal output voltage and 60% rated power vs. case temperature for minimum, nominal, and maximum input voltage.

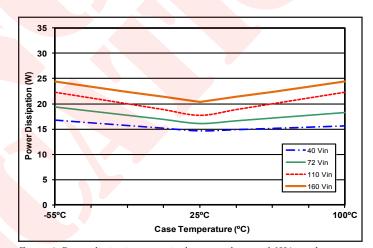


Figure 4: Power dissipation at nominal output voltage and 60% rated power vs. case temperature for minimum, nominal, and maximum input voltage.

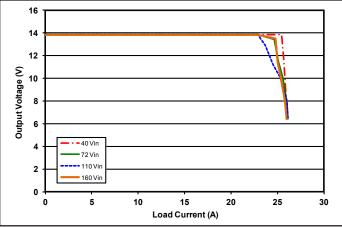


Figure 6: Output voltage vs. load current showing typical current limit curves. See Current Limit section in the Application Notes.

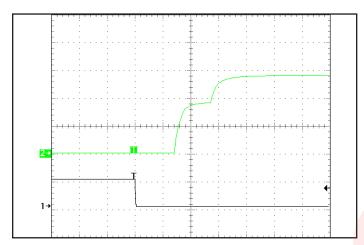


Figure 7: Typical no load startup waveform. Input voltage pre-applied, ON/OFF Pin Ch 1: (5V/div), Ch 2: (5V/div) (5mS/div)

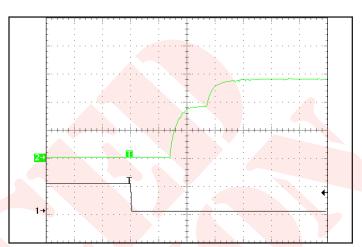


Figure 8: Typical full load startup waveform. Input voltage pre-applied, ON/OFF Pin Ch 1: (5V/div), Ch 2: (5V/div) (5mS/div).

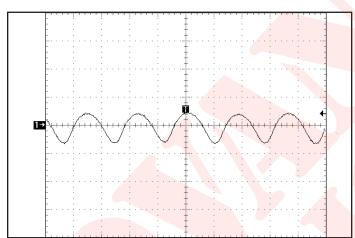


Figure 9: Input termial current ripple, at full rated output current and nominal input voltage. (500mA/div) (2uS/div).

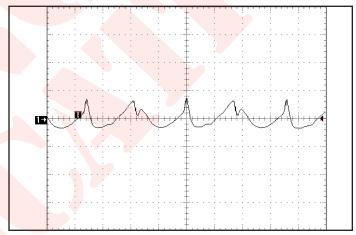


Figure 10: Output voltage ripple, Vout at nominal input voltage and rated load current (100mV/div) (2uS/div). Load capacitance: 1uF ceramic and 15uF tantalum capacitor: Bandwidth: 20MHz.

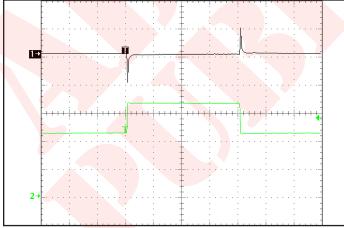


Figure 11: Output voltage response to step-change in load current (50%-75%-50% of Iout(max); dI/dt = 0.1A/uS). Load cap: $1 \mu F$ ceramic and 15 μF tantalum capacitors. Ch 1: Vout (500mV/div, Ch 2: Iout (5A/div).

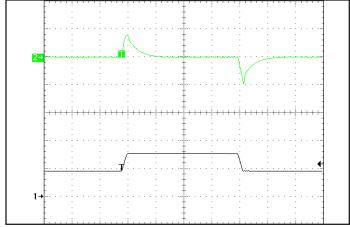


Figure 12: Output voltage response to step-change in input voltage ($1V/\mu s$). Load cap: 1 μF ceramic and 15 μF tantalum capacitors. Ch 1: Vin (100V/div), Ch 2: Vout (1V/div).

BASIC OPERATION AND FEATURES

This converter series uses a two-stage power conversion topology. The first stage is a buck-converter that keeps the output voltage constant over variations in line, load, and temperature. The second stage uses a transformer to provide the functions of input/output isolation and voltage step-up or step-down to achieve the output voltage required.

Both the first stage and the second stage switch at a fixed frequency for predictable EMI performance. Rectification of the transformer's output is accomplished with synchronous rectifiers. These devices, which are MOSFETs with a very low on-state resistance, dissipate far less energy than Schottky diodes. This is the primary reason that the converter has such high efficiency, even at very low output voltages and very high output currents.

These converters are offered totally encased to withstand harsh environments and thermally demanding applications. Conductive cooling design can be used with heat sink or cold plate cooling systems. Full power is available with baseplate temperature up to 100°C.

This series of converters use the industry standard footprint and pin-out configuration.

CONTROL FEATURES

REMOTE ON/OFF (Pin 2): The ON/OFF input, Pin 2, permits the user to control when the converter is on or off. This input is referenced to the return terminal of the input bus, Vin(-).

The ON/OFF signal is active low (meaning that a low voltage turns the converter on). Figure A details four possible circuits for driving the ON/OFF pin.

REMOTE SENSE(±) (Pins 7 and 5): The SENSE(±) inputs correct for voltage drops along the conductors that connect the converter's output pins to the load.

Pin 7 should be connected to Vout(+) and Pin 5 should be connected to Vout(-) at the point on the board where regulation is desired. A remote connection at the load can adjust for a voltage drop only as large as that specified in this datasheet, that is

Pins 7 and 5 must be connected for proper regulation of the output voltage. If these connections are not made, the converter will deliver an output voltage that is slightly higher than its specified value.

Note: The output over-voltage protection circuit senses the voltage across the output (pins 8 and 4) to determine when it should trigger, not the voltage across the converter's sense leads (pins 7 and 5). Therefore, the resistive drop on the board should be small enough so that output OVP does not trigger, even during load transients.

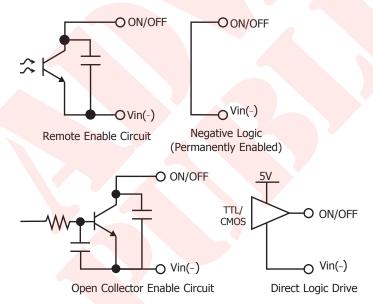


Figure A: Various circuits for driving the ON/OFF pin.

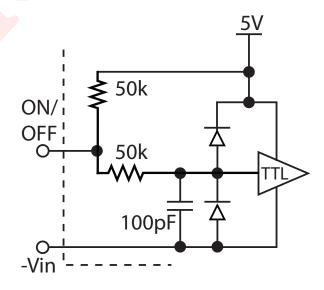


Figure B: Internal ON/OFF pin circuitry

OUTPUT VOLTAGE TRIM (Pin 6): The TRIM input permits the user to adjust the output voltage across the sense leads up or down according to the trim range specifications.

To decrease the output voltage, the user should connect a resistor between Pin 6 and Pin 5 (SENSE(-) input). For a desired decrease of the nominal output voltage, the value of the resistor should be:

Rtrim-down =
$$\left(\frac{511}{\Delta\%}\right)$$
 - 10.22 [k Ω]

where

$$\Delta\% = \left[\begin{array}{c} Vnominal - Vdesired \\ Vnominal \end{array} \right] \times 100\%$$

To increase the output voltage, the user should connect a resistor between Pin 6 and Pin 7 (SENSE(+) input). For a desired increase of the nominal output voltage, the value of the resistor should be:

Rtrim-up =
$$\left(\frac{5.11 \text{Vout x} (100 + \Delta\%)}{1.225 \Delta\%} - \frac{511}{\Delta\%} - 10.22\right)$$
 [k\Omega]

Trim graphs show the relationship between the trim resistor value and Rtrim-up and Rtrim-down, showing the total range the output voltage can be trimmed up or down.

Note: The TRIM feature does not affect the voltage at which the output over-voltage protection circuit is triggered. Trimming the output voltage too high may cause the over-voltage protection circuit to engage, particularly during transients.

It is not necessary for the user to add capacitance at the Trim pin. The node is internally filtered to eliminate noise.

Total DC Variation of Vout: For the converter to meet its full specifications, the maximum variation of the DC value of Vout, due to both trimming and remote load voltage drops, should not be greater than that specified for the output voltage trim range.

Protection Features

Input Under-Voltage Lockout: The converter is designed to turn off when the input voltage is too low, helping to avoid an input system instability problem, which is described in more detail in the application note titled "Input System Instability" on www.syngor.com. The lockout circuitry is a comparator with DC hysteresis. When the input voltage is rising, it must exceed the typical "Turn-On Voltage Threshold" value* before the converter will turn on. Once the converter is on, the input voltage must fall below the typical Turn-Off Voltage Threshold value before the converter will turn off.

Output Current Limit: The maximum current limit remains constant as the output voltage drops. However, once the impedance of the load across the output is small enough to make the output voltage drop below the specified Output DC Current-Limit Shutdown Voltage, the converter turns off.

The converter then enters a "hiccup mode" where it repeatedly turns on and off at a 5 Hz (nominal) frequency with a 5% duty cycle until the short circuit condition is removed. This prevents excessive heating of the converter or the load board.

Output Over-Voltage Limit: If the voltage across the output pins exceeds the Output Over-Voltage Protection threshold, the converter will immediately stop switching. This prevents damage to the load circuit due to 1) excessive series resistance in output current path from converter output pins to sense point, 2) a release of a short-circuit condition, or 3) a release of a current limit condition. Load capacitance determines exactly how high the output voltage will rise in response to these conditions. After 200 ms the converter will automatically restart.

Over-Temperature Shutdown: A temperature sensor on the converter senses the average temperature of the module. The thermal shutdown circuit is designed to turn the converter off when the temperature at the sensed location reaches the "Over-Temperature Shutdown" value*. It will allow the converter to turn on again when the temperature of the sensed location falls by the amount of the "Over-Temperature Shutdown Restart Hysteresis" value*.

Transient and Surge Protection: The wide input range of the RailQor line of converters covers all transient requirements of EN 50155. For short duration transients and surges found in other standards (such as RIA 12) that exceed the maximum input voltage rating of the converter, SynQor has provided a design guide for a transient suppression circuit. Please consult the application note "RailQor EN 50155 / RIA-12 Compliance & Evaluation Board Application Note" on our website www.syngor.com.

* See Electrical Characteristics page.

APPLICATION CONSIDERATIONS

Input System Instability: This condition can occur because any DC-DC converter appears incrementally as a negative resistance load. A detailed application note titled "Input System Instability" is available on the SynOor website which provides an understanding of why this instability arises, and shows the preferred solution for correcting it.

Application Circuits: Figure C below provides a typical circuit diagram which details the input filtering and voltage trimming.

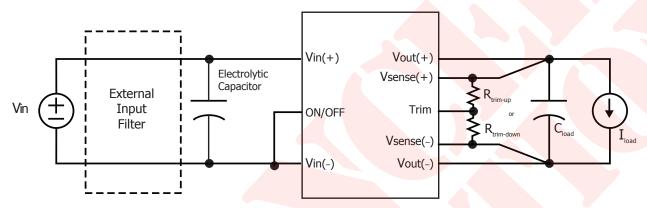


Figure C: Typical application circuit (negative logic unit, permanently enabled).

Input Filtering and External Input Capacitance: Figure D below shows the internal input filter components. This filter dramatically reduces input terminal ripple current, which otherwise could exceed the rating of an external electrolytic input capacitor.

The recommended external input capacitance is specified in the Input Characteristics section on the Electrical Specifications page. More detailed information is available in the application note titled "EMI Characteristics" on the SynQor website.

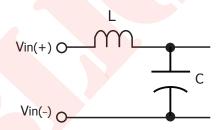


Figure D: Internal Input and Output Filter Diagram (component values listed on specifications page).

Startup Inhibit Period: The Startup Inhibit Period ensures that the converter will remain off for approximately 200 ms when it is shut down for any reason. When an output short is present, this generates a 5 Hz "hiccup mode," which prevents the converter from overheating. In all, there are six ways that the converter can be shut down, initiating a Startup Inhibit Period:

- Input Under-Voltage Lockout
- Output Over-Voltage Protection
- Over Temperature Shutdown
- Current Limit
- Short Circuit Protection
- Turned off by the ON/OFF input

Figure E shows three turn-on scenarios, where a Startup Inhibit Period is initiated at t₀, t₁, and t₂:

Before time t_o, when the input voltage is below the UVL threshold, the unit is disabled by the Input Under-Voltage Lockout feature. When the input voltage rises above the UVL threshold, the Input Under-Voltage Lockout is released, and a Startup Inhibit Period is initiated. At the end of this delay, the ON/OFF pin is evaluated, and since it is active, the unit

At time t₁, the unit is disabled by the ON/OFF pin, and it cannot be enabled again until the Startup Inhibit Period has elapsed.

When the ON/OFF pin goes high after t₂, the Startup Inhibit Period has elapsed, and the output turns on within the typical Turn-On Time.

Thermal Considerations: The maximum operating baseplate temperature, T_R, is 100 °C. As long as the user's thermal system keeps T_R < 100 °C, the converter can deliver its full rated power.

A power derating curve can be calculated for any heatsink that is attached to the baseplate of the converter. It is only necessary to determine the thermal resistance, $R_{TH-BA'}$, of the chosen heatsink between the baseplate and the ambient air for a given airflow rate. This information is usually available from the heatsink vendor. The following formula can the be used to determine the maximum power the converter can dissipate for a given thermal condition if its baseplate is to be no higher than 100 °C.

$$P_{diss}^{max} = \frac{100 \text{ °C - TA}}{R_{TH-RA}}$$

This value of power dissipation can then be used in conjunction with the data shown in Figure 2 to determine the maximum load current (and power) that the converter can deliver in the given thermal condition.

For convenience, power derating curves for an encased converter without a heatsink are provided for each output voltage.

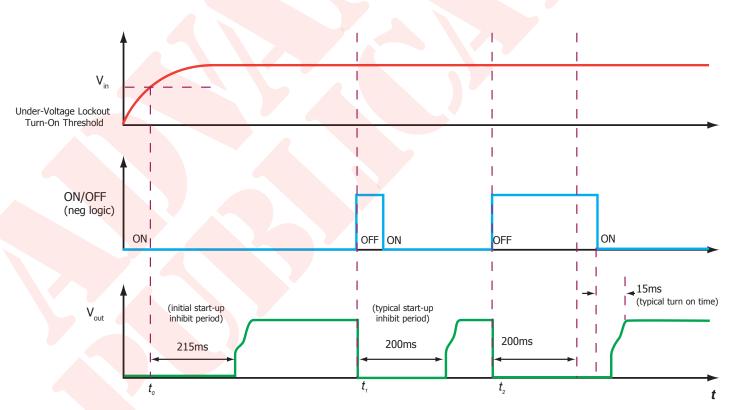


Figure E: Startup Inhibit Period (turn-on time not to scale)

Designing for Maximizing Available Power: RailQor products have been designed for full power operation in demanding thermal environments. However, there are techniques that can be applied external to the converter to ensure the best possible thermal performance. These include correctly applying a heatsink to the baseplate of the converter and maximizing the heat transferred through the pins. The following discussions are only guidelines and may not be necessary depending on the application

Optimal Heatsink Application: There are two key components to maximizing the thermal dissipation when using a heatsink. One is minimizing the thermal resistance between the converter and the heatsink itself. There are imperfections between the mating surfaces that reduce contact area between the two. A suitable thermally conductive interface material should be used to maintain a good thermal connection. A commonly used example is thermal grease. When utilizing the threaded inserts on the baseplate of a SynQor converter, care must be taken not to exceed the torque or screw depth guidelines found in the mechanical diagram. Two, airflow must be directed to pass between the fins of the heatsink to maximize the surface area for heat removal.

Heatsinks are often available with both transverse and longitudinal fin direction to allow system flexibility. Care should be taken to avoid large external components surrounding the converter from blocking airflow.

Layout Considerations: Significant performance improvement can be made by designing a printed circuit board to properly sink heat away from the converter through its pins. The first step is to ensure a correctly formed solder joint at each pin. A smooth fillet and complete barrel fill should be observed at the boundary of pin and mounting hole to ensure maximum heat conduction from pin to board (Figure F). It is worth noting here that encased SynQor products are not compatible with reflow processes as it may disrupt the placement of internal components.

The board itself should also have as many layers and as high of copper weight as is practical for the application. Large ground and power planes are best as the most heat will be conducted through the large power pins of the converter on both input and output sides. The heat must also have a path to conduct from the copper planes of the board to the outside environment. The typical FR4 material used in construction of a printed circuit board is greater than 1000 times less thermally conductive than copper and will act as an insulator between each copper plane. To mitigate this, generous use of thermal vias is recommended in the board area surrounding and below the converter. A proper density of vias allows heat to conduct from the board to the air while maintaining a large amount of copper area to conduct to the vias. For reference, boards used in SynQor thermal testing are 6 layer, 2 oz. copper boards with 50 mil diameter thermal vias at a density of 36/in² (Figure G).

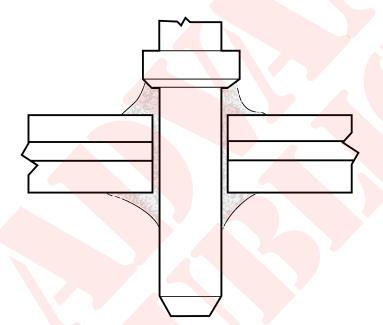


Figure F: Example of properly soldered pin joint

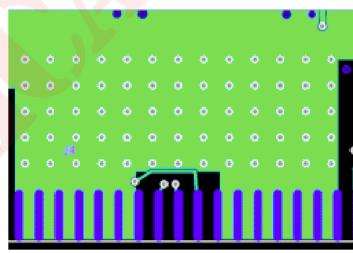


Figure G: Image of thermal via layout surrounding converter in test board



FULL FEATURE APPLICATION NOTES

This section provides some basic application information for the full-feature version of the RailQor series converter. All units in this product family include back-drive protection to simplify the user of multiple converters in a parallel or sequencing application. However, any voltage applied to the output of the converter should be kept below 120% of the rated output voltage of the converter.

In addition to back-drive protection, these units include the following features (pins):

Current Share (pin C): The active current share feature allows for N + 1 and parallel applications. To achieve load sharing, directly connect the I share pins of multiple units. The load current will share equally among the multiple units (±5% at full rated current). It is important that the Vin(-) pins of the sharing units be directly connected and NOT placed outside of an EMI filter or other impedance path. The voltage at the I Share pin will range from 0 to 5 volts (at full rated current), referenced to the primaryside ground, Vin(-).

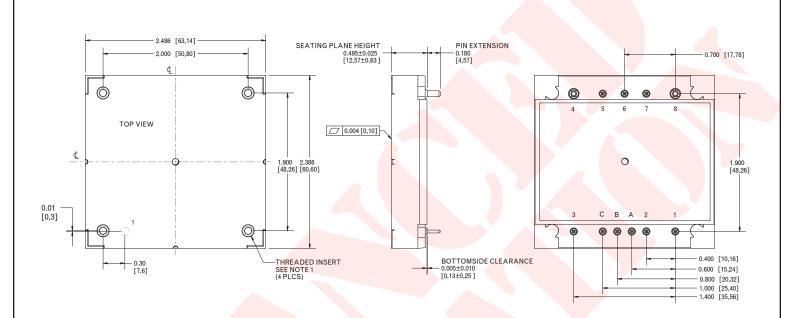
Start Sync (pin B): The Start Synchronization pin will allow a more consistent start-up sequence. To operate this feature, connect together the Start-Sync pins of multiple current-sharing units. This will permit immediate start-up with loads greater than the current limit of a single unit. Without this connection, any set of converters attempting to asynchronously start (or restart) with a load greater than the current limit of a single unit will "hiccup". This "hiccup" mode will continue until one converter attempts a start at the same time as the minimum number of additional units necessary to sustain the load condition. For example, three 50 amp units starting into a 90 amp load would require two units to simultaneously attempt a start. The Start Sync connection synchronizes these starting attempts and provides a more consistent and reliable start-up sequence. For details about the "hiccup" mode ore repeated startup attempts, please see the "Startup Inhibit Period" note in the Technical Specification.

Clock Sync (pin A): The External Clock Synchronization pin provides the ability for the user to control the EMI signature and synchronize sensitive circuitry to quiet periods in the converter operation. With this option, the converter can be synchronized to an external clock signal whose frequency is greater than that of the free-running internal clock. However, substantially raising the converter's frequency will reduce its efficiency. Therefore, the recommended frequency range for the external clock synchronization signal applied to this pin is listed in the Technical Specification.

The following requirements should also be met:

- The external clock signal should be referenced to the negative input voltage, Vin(-).
- The high level of the signal should be between 3.5V and 5.0V.
- The low level should be between -0.5V and +1.2V.
- Do not apply a clock signal lower than the specified frequency.

Clock Sync Signal should be applied before unit turn on.



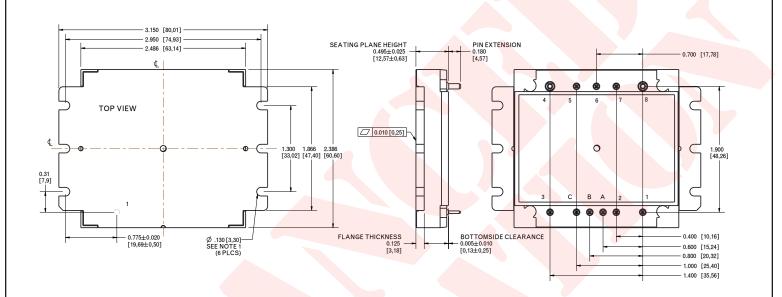
- 1) APPLIED TORQUE PER M3 SCREW NOT TO EXCEED 6in-lb LIMIT (0.7Nm)
- 2) BASEPLATE FLATNESS TOLERANCE IS 0.004" (.10mm) TIR FOR SURFACE.
- 3) PINS 1-3, 5-7, A, B, AND C ARE 0.040" (1.02mm) DIA. WITH 0.080" (2.03mm) DIA. STANDOFFS.
- 4) PINS 4 AND 8 ARE 0.080" (2.03mm) DIA. WITH 0.125" (3.18mm) DIA STANDOFFS
- 5) ALL PINS: MATERIAL: COPPER ALLOY FINISH: MATTE TIN OVER NICKEL PLATE
- 6) WEIGHT: 5.0 oz. (142 g) TYPICAL
- 7) ALL DIMENSIONS IN INCHES(mm)
- 8) TOLERANCES: X.XXIN +/-0.02 (X.Xmm +/-0.5mm) X.XXXIN +/-0.010 (X.XXmm +/-0.25mm)

PIN DESIGNATIONS

	Label	Name	Function
1	+VIN	Vin(+)	Positive input voltage
2	ON/OFF	ON/OFF	TTL input to turn converter on and off, referenced to Vin(–), with internal pull up.
Α	CLOCK SYNC	CLOCK SYNC	Clock synchronization
В	START SYNC	START SYNC	Startup synchronization
О	SHARE	I SHARE	Single wire parallel signal
3	IN RTN	Vin(-)	Input Return
4	OUT RTN	Vout(-)	Output Return
5	-SNS	SENSE(-)	Negative remote sense ¹
6	TRIM	TRIM	Output voltage trim ²
7	+SNS	SENSE(+)	Positive remote sense ³
8	+VOUT	Vout(+)	Positive output voltage

Notes:

- 1) SENSE(-) should be connected to Vout(-) either remotely or at the converter.
- 2) Leave TRIM pin open for nominal output voltage.
- 3) SENSE(+) should be connected to Vout(+) either remotely or at the converter.
- 4) Pins A, B, and C are populated on Full Feature version only.



- 1) APPLIED TORQUE PER M3 OR 4-40 SCREW IS NOT TO EXCEED 6in-lb (0.7Nm)
- 2) BASEPLATE FLATNESS TOLERANCE IS 0.010" (.25mm) TIR FOR SURFACE.
- 3) PINS 1-3, 5-7, A, B, AND C ARE 0.040" (1.02mm) DIA. WITH 0.080" (2.03mm) DIA. STANDOFFS.
- 4) PINS 4 AND 8 ARE 0.080" (2.03mm) DIA. WITH 0.125" (3.18mm) DIA STANDOFFS
- 5) ALL PINS: MATERIAL: COPPER ALLOY FINISH: MATTE TIN OVER NICKEL PLATE
- 6) WEIGHT: 5.2 oz. (147 g) TYPICAL
- 7) ALL DIMENSIONS IN INCHES(mm)
- 8) TOLERANCES: X.XXIN +/-0.02 (X.Xmm +/-0.5mm) X.XXXIN +/-0.010 (X.XXmm +/-0.25mm)

PIN DESIGNATIONS

	Label	Name	Function
1	+VIN	Vin(+)	Positive input voltage
2	ON/OFF	ON/OFF	TTL input to turn converter on and off, referenced to Vin(–), with internal pull up.
Α	CLOCK SYNC	CLOCK SYNC	Clock synchronization
В	START SYNC	START SYNC	Startup synchronization
С	SHARE	I SHARE	Single wire parallel signal
3	IN RTN	Vin(-)	Input Return
4	OUT RTN	Vout(-)	Output Return
5	-SNS	SENSE(-)	Negative remote sense ¹
6	TRIM	TRIM	Output voltage trim ²
7	+SNS	SENSE(+)	Positive remote sense ³
8	+VOUT	Vout(+)	Positive output voltage

Notes:

- 1) SENSE(-) should be connected to Vout(-) either remotely or at the converter.
- 2) Leave TRIM pin open for nominal output voltage.
- 3) SENSE(+) should be connected to Vout(+) either remotely or at the converter.
- 4) Pins A, B, and C are populated on Full Feature version only.

STANDARDS COMPLIANCE

Parameter	Notes & Conditions		
STANDARDS COMPLIANCE		Pending	
EN 60950-1			
UL 60950-1			
CAN/CSA C22 2 No. 60950-1			

Note: An external input fuse must always be used to meet these safety requirements. Contact SynQor for official safety certificates on new releases or download from the SynQor website.

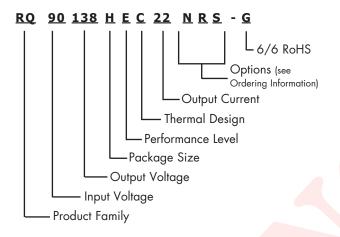
QUALIFICATION TESTING

Parameter	# Units	Test Conditions
QUALIFICATION TESTING		
Vibration	5	EN 61373:1999 Category I, Class B, Body mounted
Life Test	30	95% rated Vin and load, units at derating point, 1000 hours
Cold	5	EN 60068-2-1:2007
Dry Heat	5	EN 60068-2-2:2007
Mechanical Shock	5	EN 61373:1999 Category I, Class B, Body mounted
Temperature Cycling	5	-40 °C to 100 °C, unit temp. ramp 15 °C/min., 500 cycles
Power/Thermal Cycling	5	Toperating = min to max, Vin = min to max, full load, 100 cycles
Design Marginality	5	Tmin-10 °C to Tmax+10 °C, 5 °C steps, Vin = min to max, 0-105% load
Damp Heat, Cyclic	5	EN 60068-2-30:2005
Solderability	15 pins	MIL-STD-883, method 2003.8

Note: Governing Standard BS EN 50155:2007 Railway applications - Electronic equipment used on rolling stock

PART NUMBERING SYSTEM

The part numbering system for SynQor's dc-dc converters follows the format shown in the example below.



ORDERING INFORMATION

The tables below show the valid model numbers and ordering options for converters in this product family. When ordering SynQor converters, please ensure that you use the complete 15 character part number consisting of the 12 character base part number and the additional characters for options. Add "-G" to the model number for 6/6 RoHS compliance.

Mod	Model Number		Output Voltage	Max Output Current
RQ9	0138HEw22xyz	40-160V	13.8V	21.7A

The first 12 characters comprise the base part number and the last 3 characters indicate available options. The "-G" suffix indicates 6/6 RoHS compliance.

Application Notes

A variety of application notes and technical white papers can be downloaded in pdf format from our website.

RoHS Compliance: The EU led RoHS (Restriction of Hazardous Substances) Directive bans the use of Lead, Cadmium, Hexavalent Chromium, Mercury, Polybrominated Biphenyls (PBB), and Polybrominated Diphenyl Ether (PBDE) in Electrical and Electronic Equipment. This SynQor product is 6/6 RoHS compliant. For more information please refer to SynQor's RoHS addendum available at our RoHS Compliance / Lead Free Initiative web page or e-mail us at rohs@synqor.com.

The following options must be included in place of the **w** x y z spaces in the model numbers listed above. Not all combinations make valid part numbers, please contact SynQor for availability.

	Options Description						
	Thermal Design Enable Logic Pin Style Feature Set						
L	w	X	y	Z			
	C - Encased D - Encased with Non-Threaded Baseplate / - Encased with Flanged Baseplate	N - Negative	R - 0.180"	S – Standard Feature F – Full Feature			

Contact SynQor for further information and to order:

Phone: 978-849-0600
Toll Free: 888-567-9596
Fax: 978-849-0602

E-mail: power@synqor.com **Web**: www.synqor.com **Address**: 155 Swanson Road

Boxborough, MA 01719

USA

PATENTS

SynQor holds numerous U.S. patents, one or more of which apply to most of its power conversion products. Any that apply to the product(s) listed in this document are identified by markings on the product(s) or on internal components of the product(s) in accordance with U.S. patent laws. SynQor's patents include the following:

5,999,417	6,222,742	6,545,890	6,594,159	6,894,468	6,896,526
6,927,987	7,050,309	7,072,190	7,085,146	7,119,524	7,269,034
7,272,021	7,272,023	7,558,083	7,564,702	7,765,687	7,787,261
8.023.290	8.149.597	8.493.751	8.644.027	9.143.042	

WARRANTY

SynQor offers a two (2) year limited warranty. Complete warranty information is listed on our website or is available upon request from SynQor.